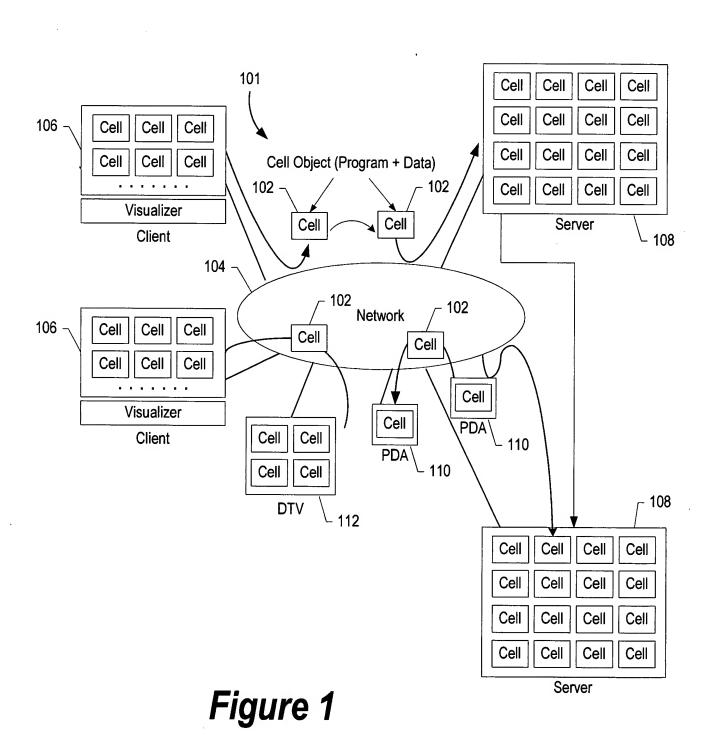
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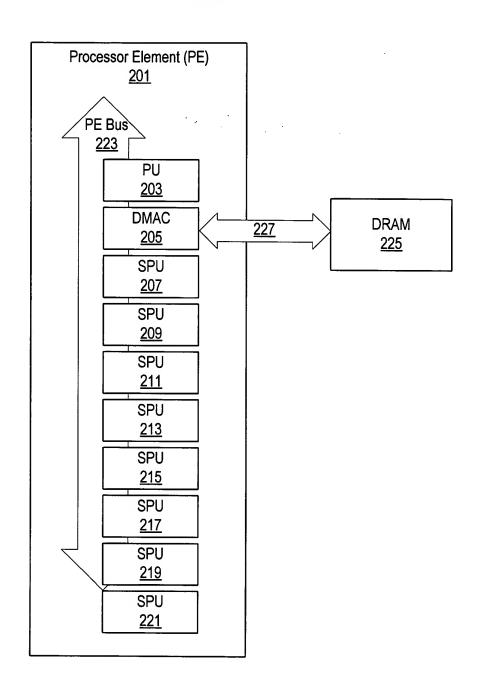


Figure 2

D cket N . AUS920030790US1

Min r, et al.

Syst m and Meth df r Balancing C mputati nal L ad Across a Plurality of Pr cess rs

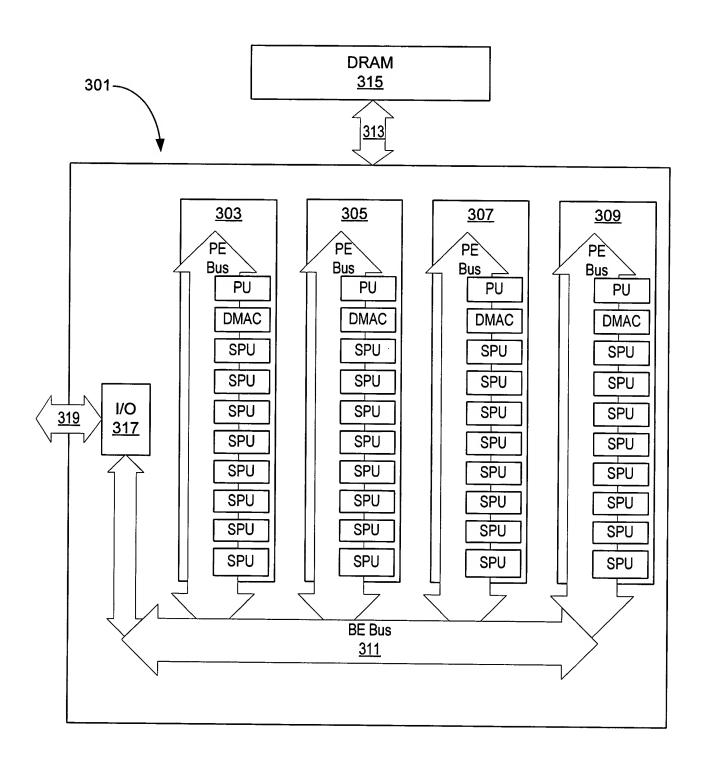


Figure 3

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System and M th d for Balancing C mputational Load Acr ss a Plurality f Pr c ss rs

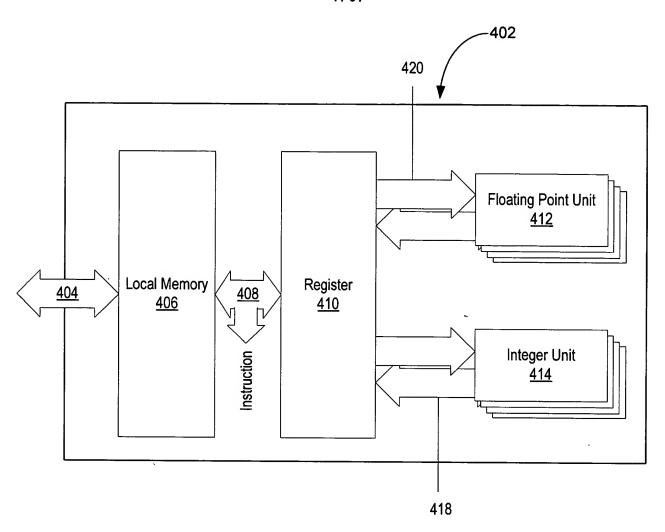


Figure 4

Dock tN . AUS920030790US1 Min r, tal.

System and Meth df r Balancing Computati nal Load Acr ss a Plurality of Pr c ssors

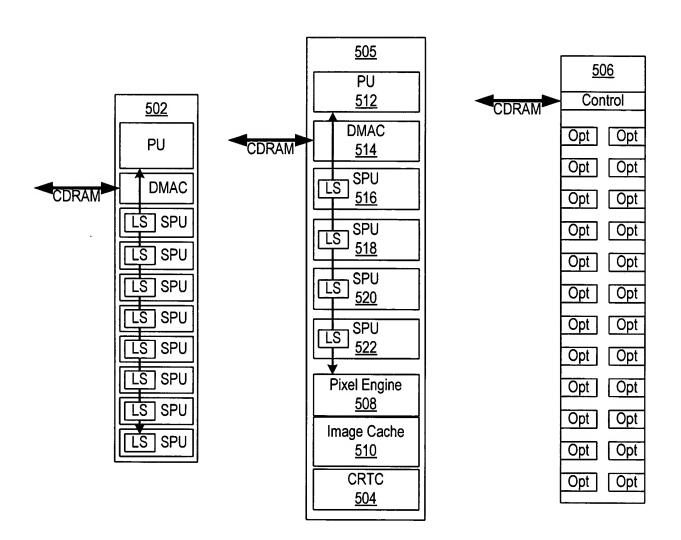
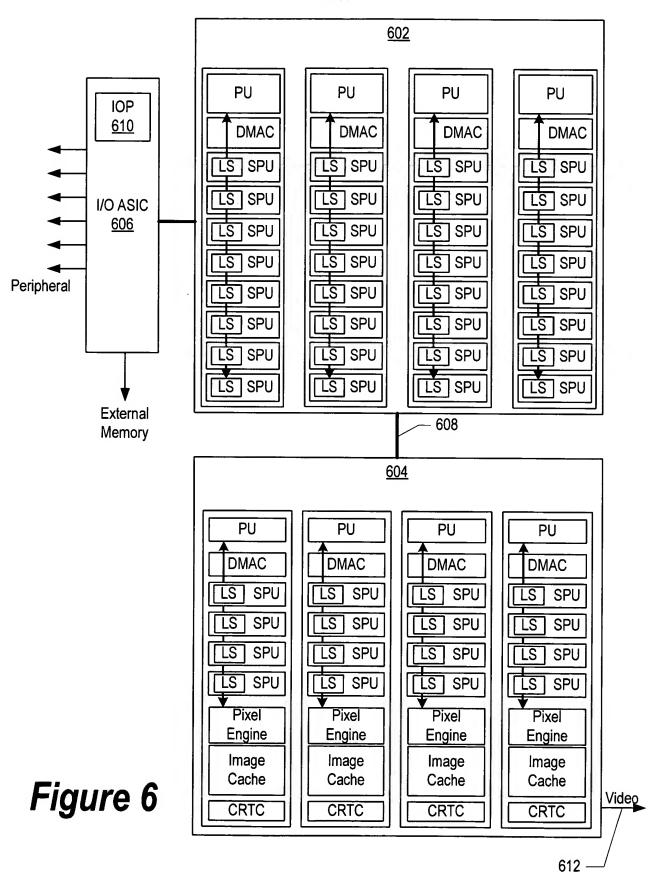


Figure 5

D cket No. AUS920030790US1 Minor, et al. Syst m and M th d f r Balancing C mputati nal L ad Across a Plurality of Proc ssors



D ck t No. AUS920030790US1 Min r, et al.

System and M th df r Balancing Computati nal Load Acr ss a Plurality f Pr c ss rs

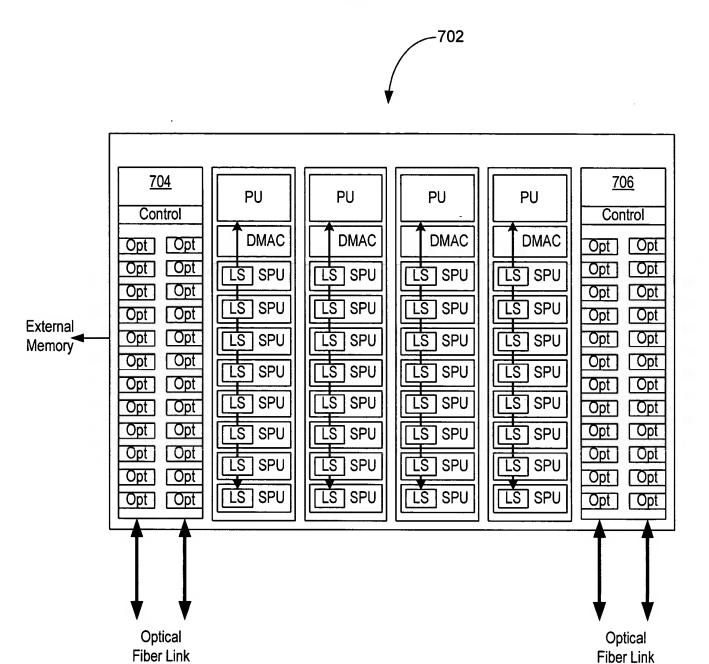


Figure 7

D ck t No. AUS920030790US1 Min r, et al. Syst m and Meth d for Balancing C mputational Load Acr ss a Plurality f Pr c ss rs 8/51

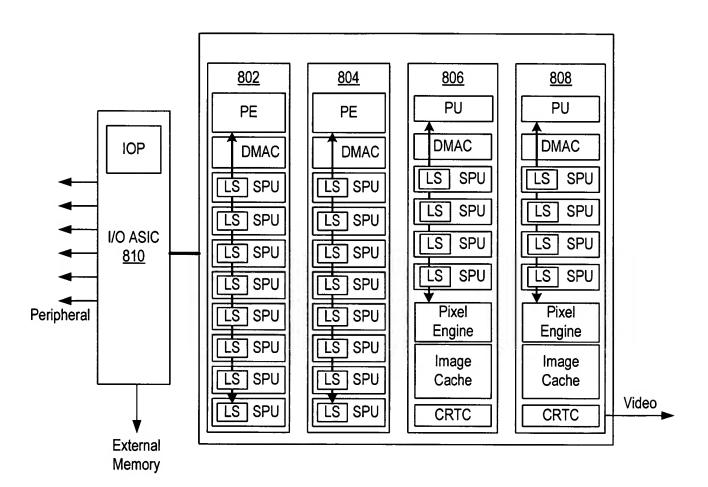


Figure 8

Docket No. AUS920030790US1

Minor, t al.

System and M th d for Balancing Computational L ad Acr ss a Plurality f Pr c ss rs

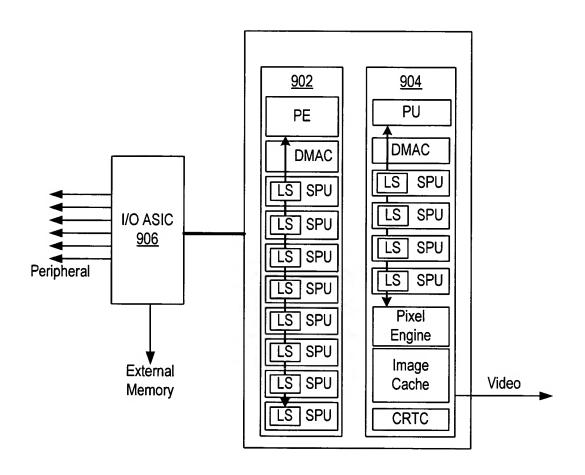


Figure 9

Docket No. AUS920030790US1

Minor, tal.

Syst m and Meth df r Balancing C mputati nal Load Acr ss a Plurality f Proc ssors

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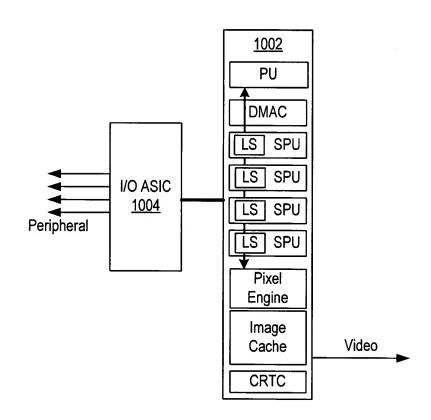
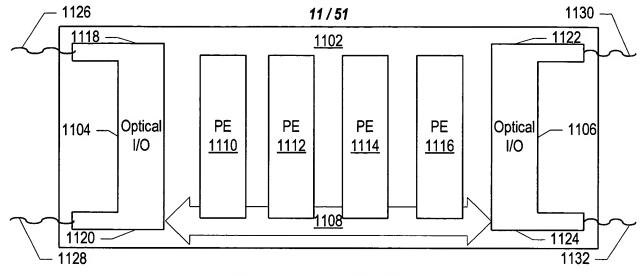


Figure 10

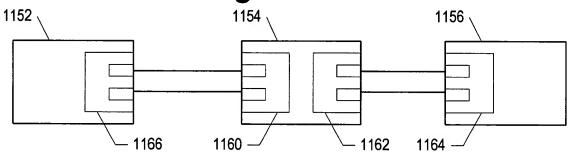
D cket No. AUS920030790US1

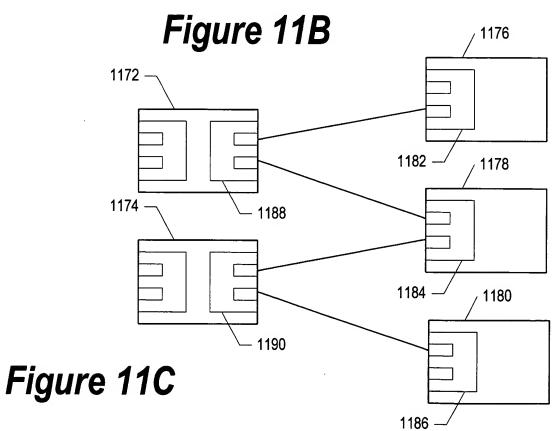
Min r, et al.

System and Meth df r Balancing C mputati nal L ad Acr ss a Plurality of Proc ssors





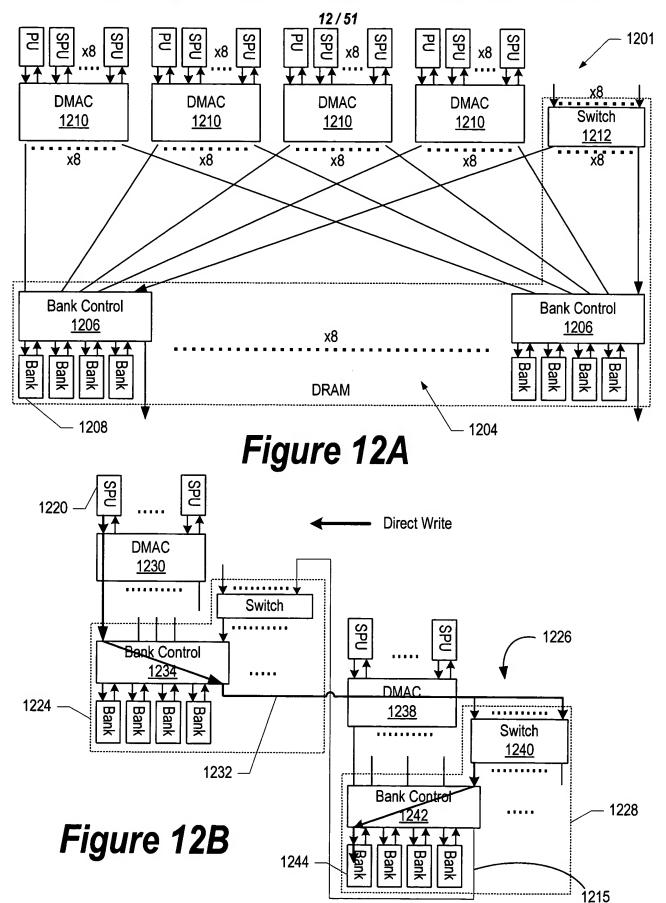




D ck t No. AUS920030790US1

Min r, t al.

Syst m and Meth d f r Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs



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1320 1322 1324 / 1326 1328 1330 1332 1334 PU Bank Bank Bank Bank Bank Bank Bank - 1302 Bank SPU Bank Bank Bank Bank Bank Bank Bank Bank - 1304 SPU Bank Bank Bank Bank Bank Bank Bank Bank 1306 SPU Bank Bank Bank Bank Bank Bank Bank Bank - 1308 SPU Bank Bank Bank Bank Bank Bank Bank Bank - 1310 SPU Bank Bank Bank Bank Bank Bank Bank Bank - 1312 SPU Bank Bank Bank Bank Bank Bank Bank Bank 1314 SPU Bank Bank Bank Bank Bank Bank Bank Bank - 1316 SPU DRAM

Figure 13

Dock t N . AUS920030790US1

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Syst m and M th d f r Balancing C mputati nal L ad Acr ss a Plurality f Proc ssors

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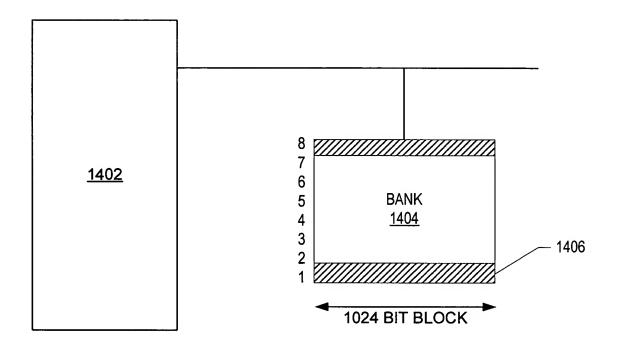


Figure 14A

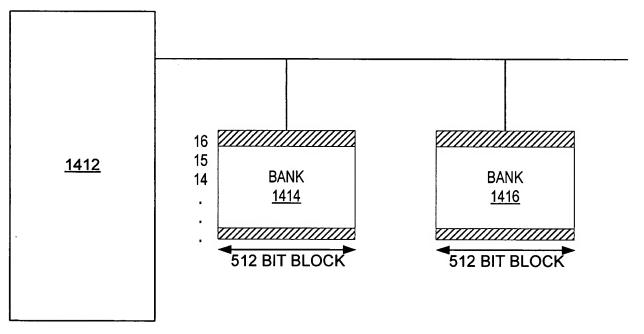


Figure 14B

D cket N . AUS920030790US1

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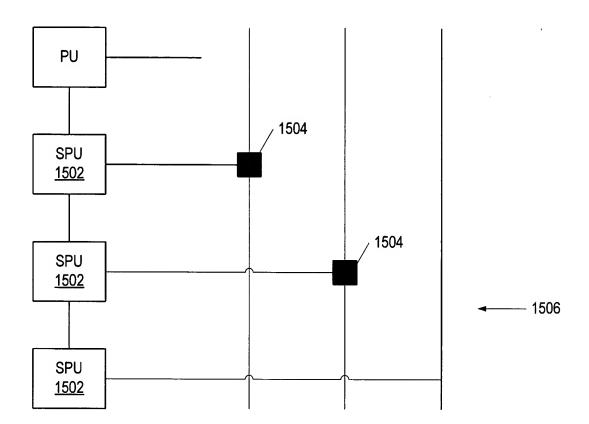


Figure 15

Dock t N . AUS920030790US1

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Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Proc ss rs

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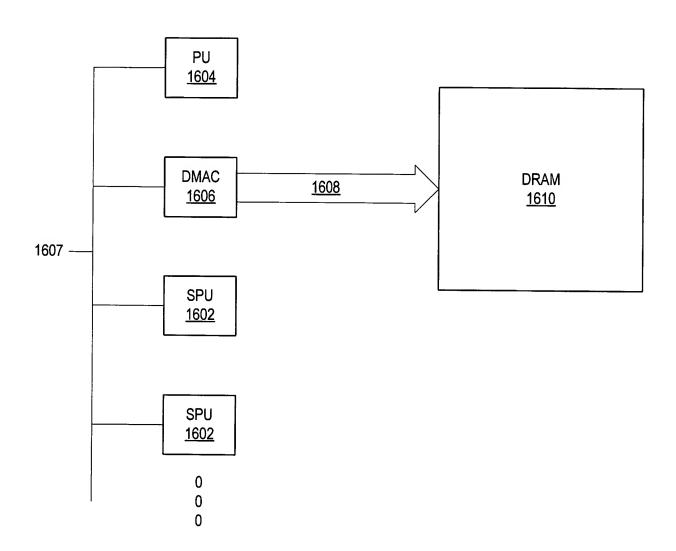
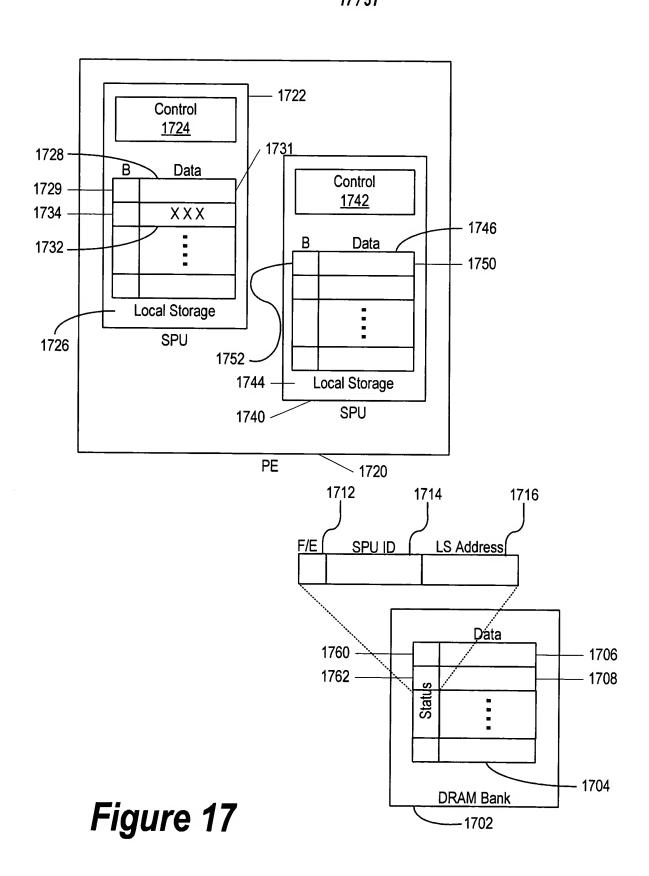


Figure 16

Docket No. AUS920030790US1

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Syst m and M th d f r Balancing C mputati nal Load Acr ss a Plurality f Pr cess rs



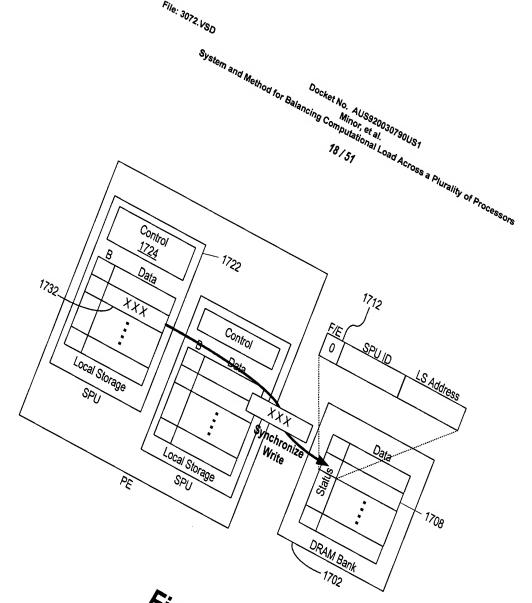


Figure 18

Docket N . AUS920030790US1

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System and Meth dfrBalancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs

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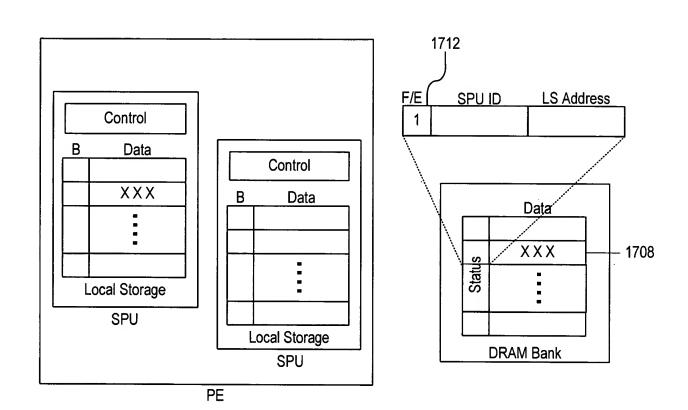
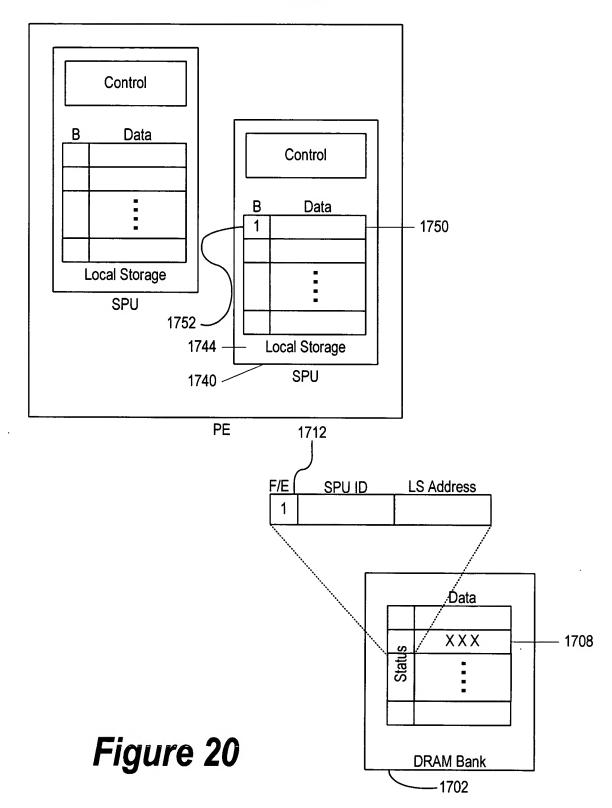


Figure 19

Dock t No. AUS920030790US1

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System and Meth d for Balancing C mputational L ad Across a Plurality f Pr cess rs



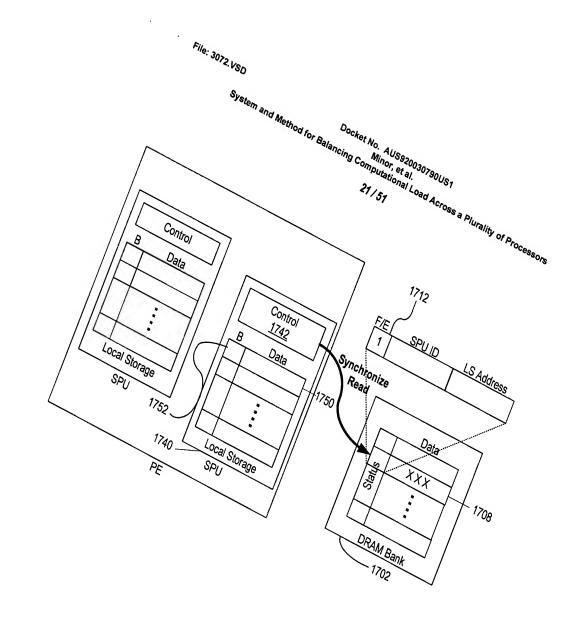
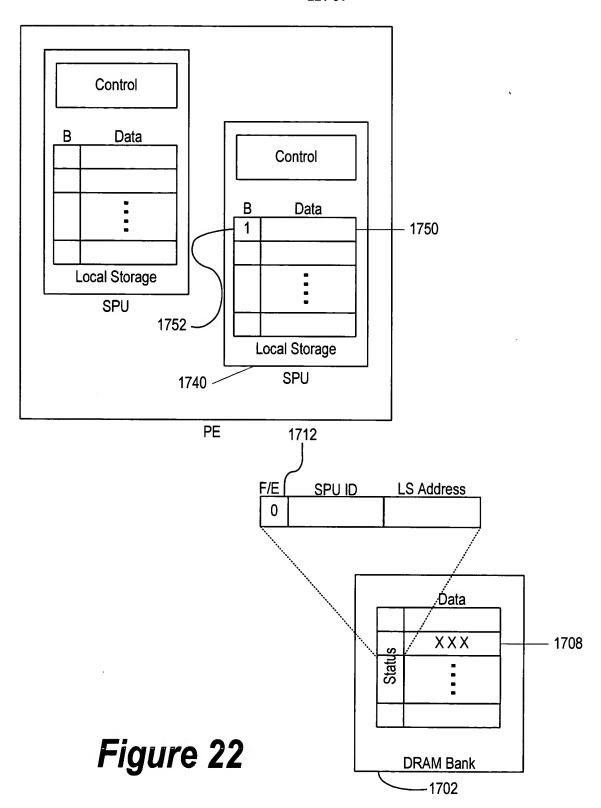
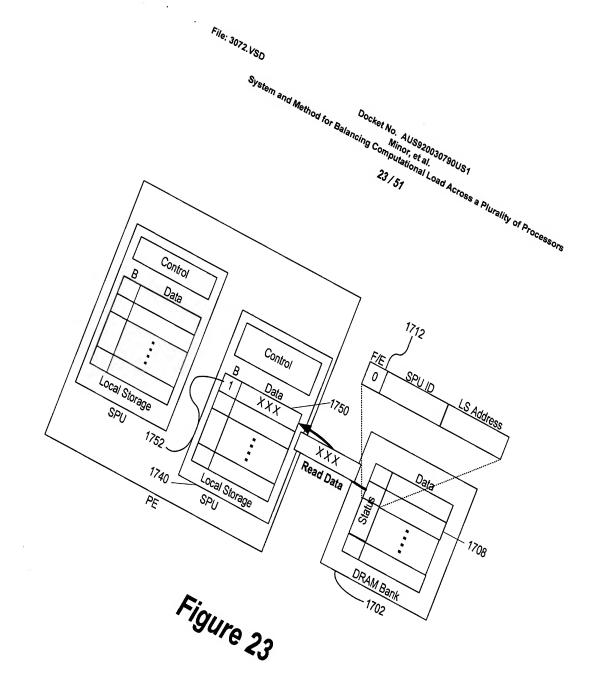


Figure 21

Dock t No. AUS920030790US1 Min r, et al.

System and Meth df r Balancing C mputati nal L ad Across a Plurality of Pr c ss rs

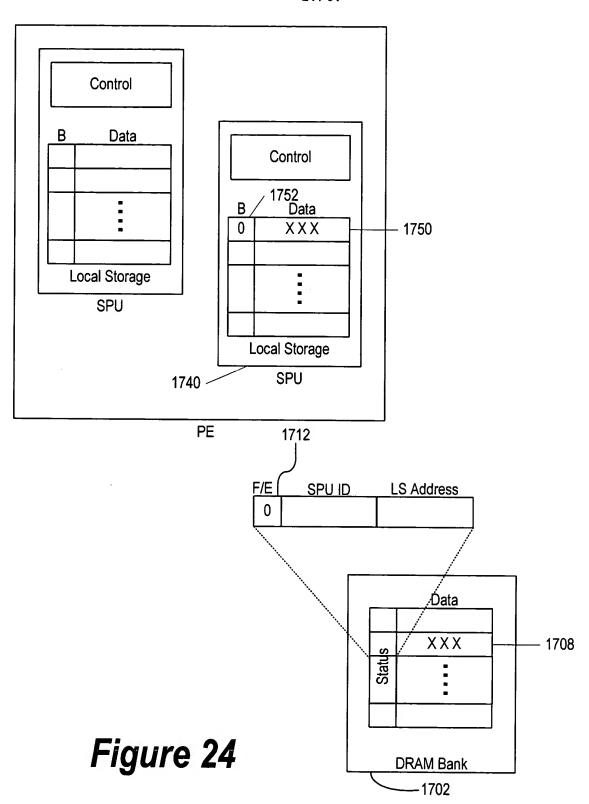




D cket No. AUS920030790US1

Min r, t al.

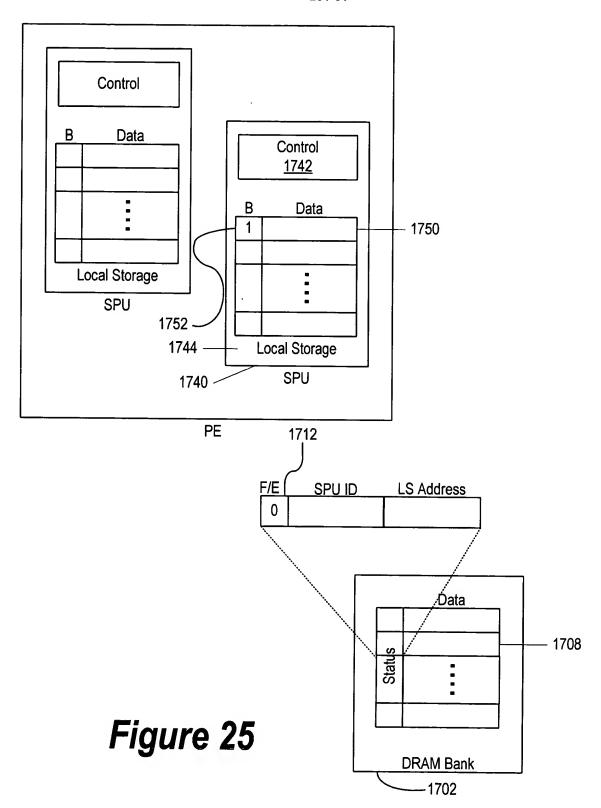
Syst m and Method f r Balancing C mputati nal L ad Acr ss a Plurality of Pr cess rs



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Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Pr cessors



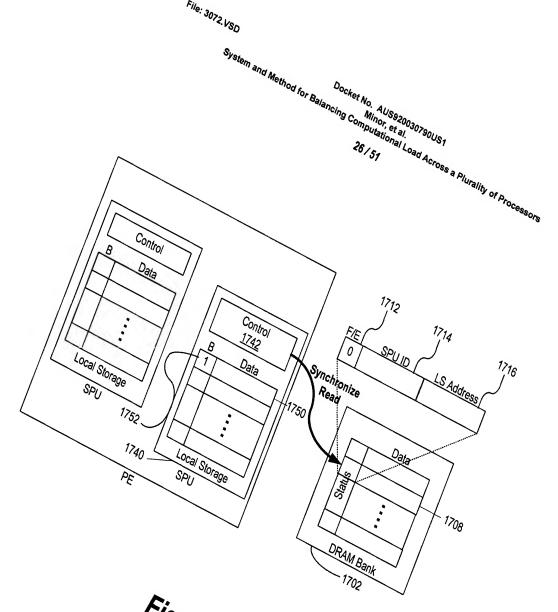


Figure 26

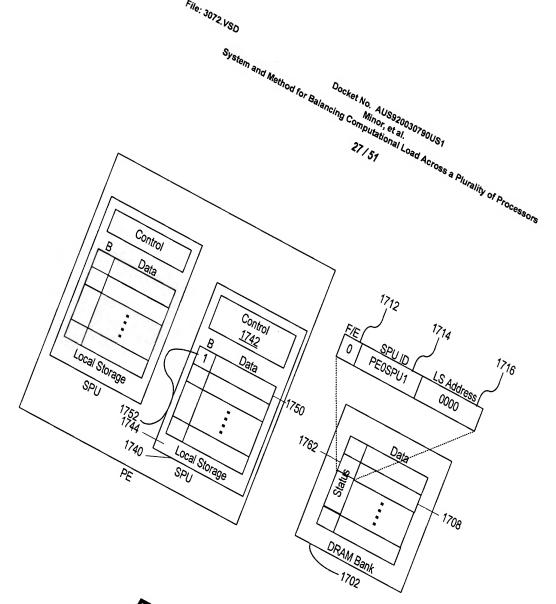


Figure 27

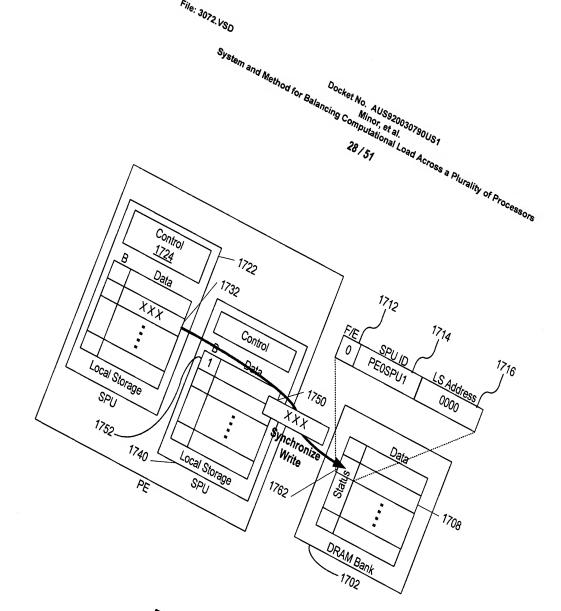


Figure 28

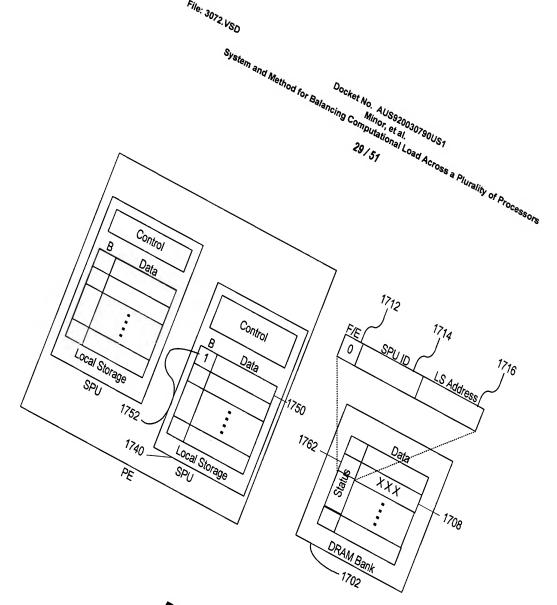


Figure 29

Dock t N . AUS920030790US1

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System and Method f r Balancing C mputati nal Load Acr ss a Plurality f Pr c ss rs

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1712 1714 1716 SPU ID LS Address Control 0 В Data Control 1750 Data Data XXXXXX- 1708 **Read Data** Local Storage SPU 1752 Local Storage **DRAM Bank** 1740 -SPU **— 1702** PE

Figure 30

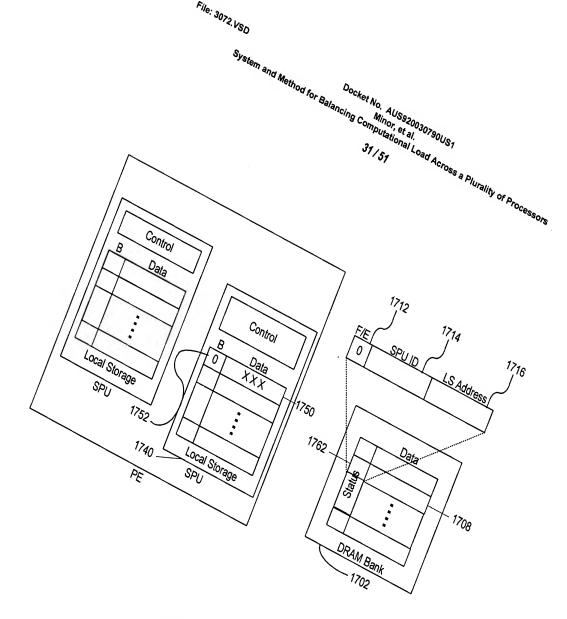


Figure 31

Docket No. AUS920030790US1

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System and Meth d f r Balancing Computati nal L ad Acr ss a Plurality of Pr cess rs

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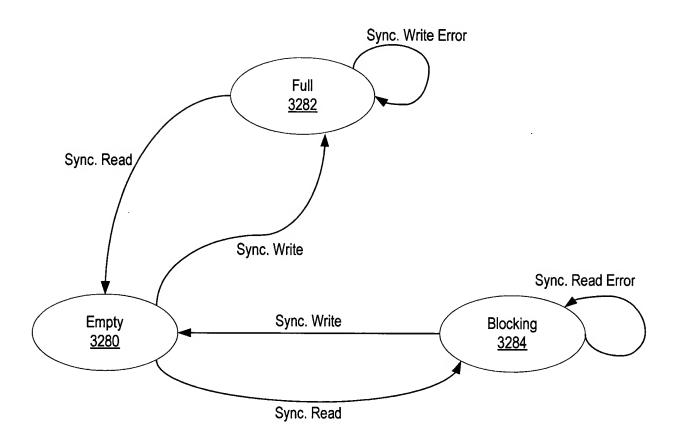


Figure 32

D cket No. AUS920030790US1 Min r, t al.

Syst m and Method f r Balancing C mputati nal L ad Acr ss a Plurality of Pr c ssors

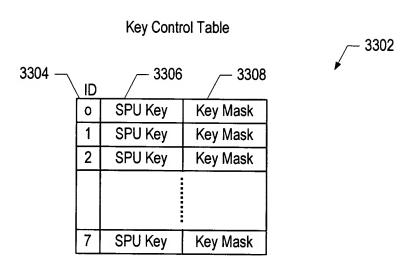


Figure 33

D cket N . AUS920030790US1

Minor, tal.

Syst m and M th d f r Balancing C mputati nal L ad Across a Plurality f Pr c ssors

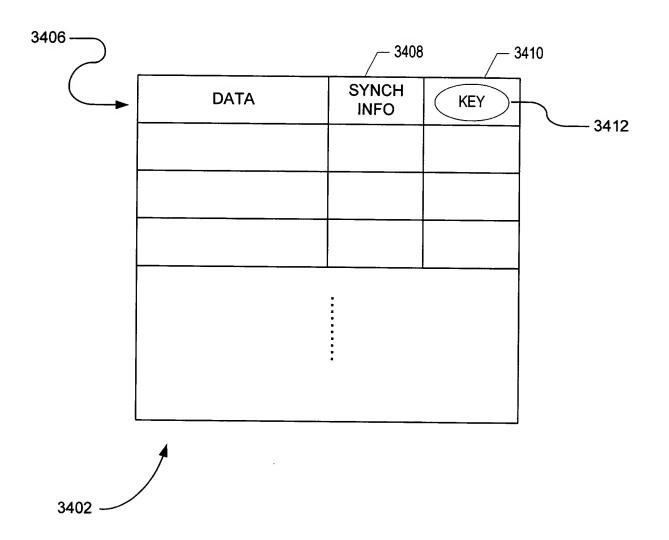


Figure 34

D cket No. AUS920030790US1 Min r, et al.

System and M thod f r Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs

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Memory Access Control Table

3504 —		3506	_	3508 3510	3512
	\ID			/	
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
	63	Base	Size	Access Key	Access Key Mask

3502

Figure 35

D ck t No. AUS920030790US1 Min r, et al. Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs

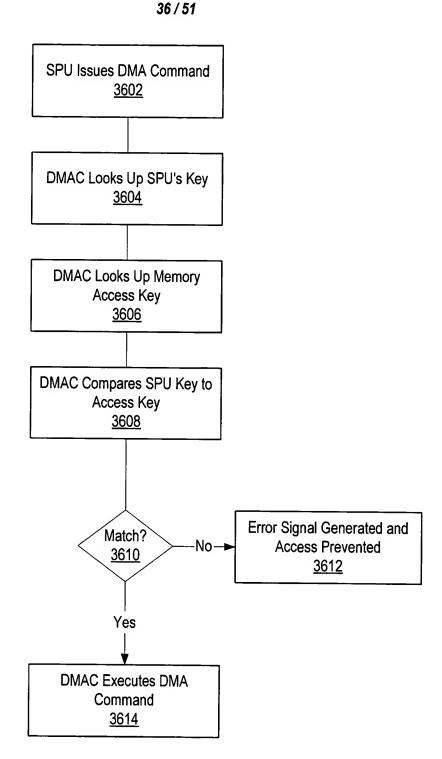


Figure 36

Docket No. AUS920030790US1

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System and M thod f r Balancing C mputati nal L ad Acr ss a Plurality f Process rs

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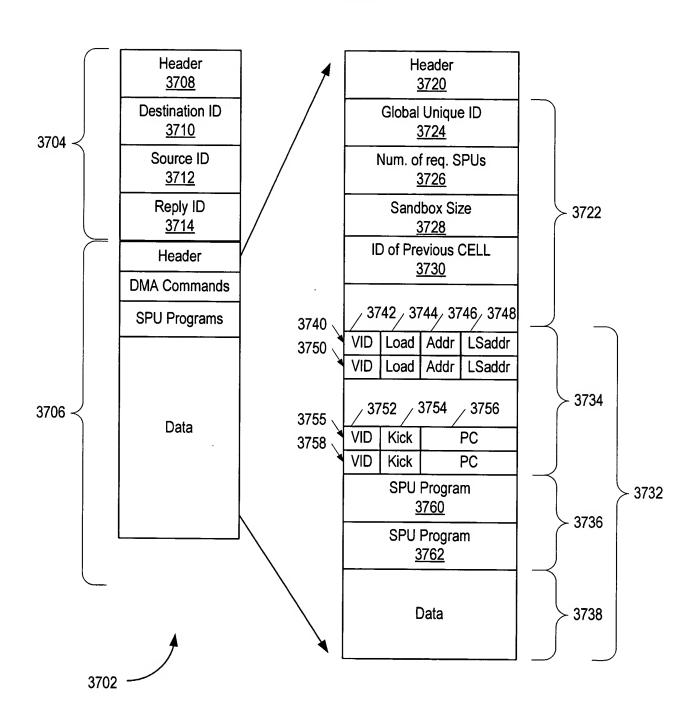


Figure 37

D cket No. AUS920030790US1

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System and Meth d for Balancing C mputati nal Load Acr ss a Plurality f Pr c ssors

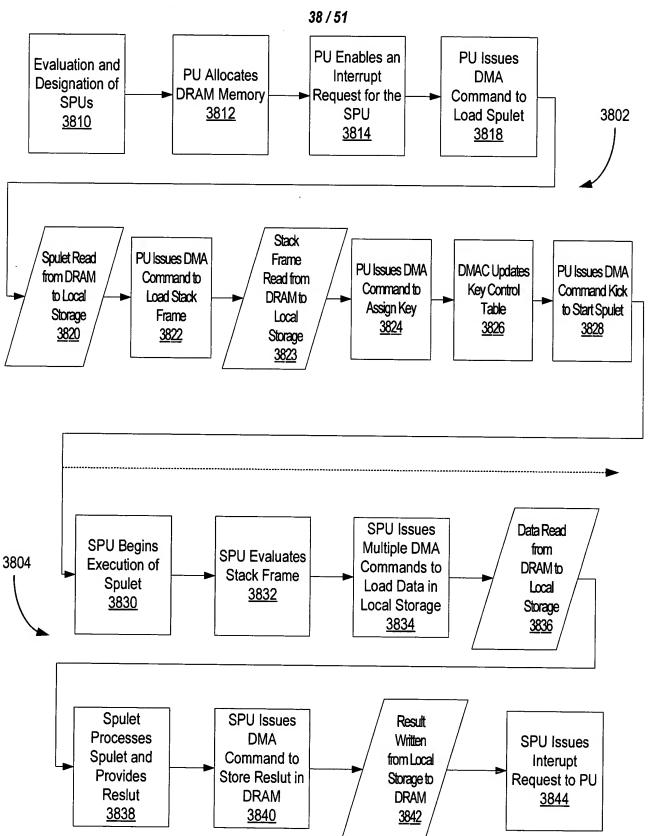


Figure 38

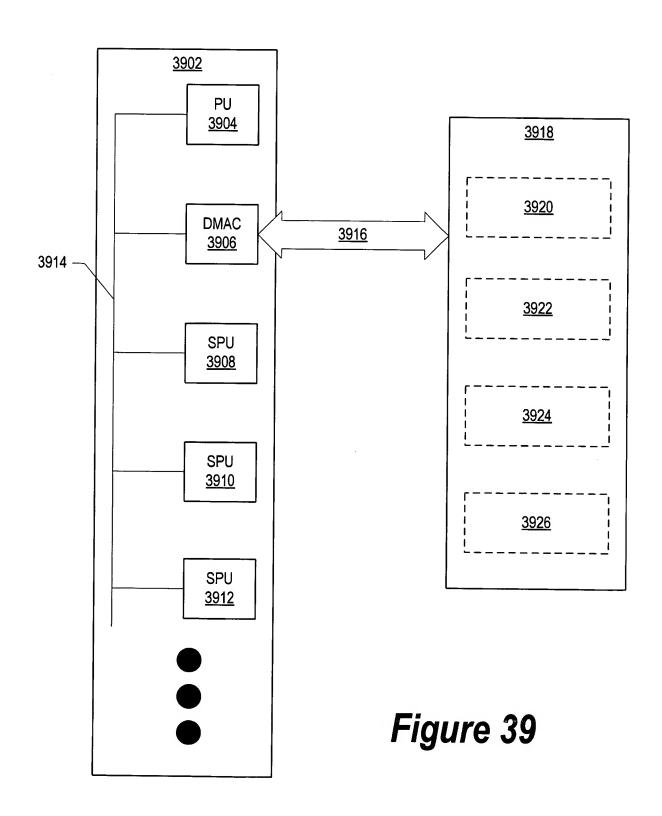
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D ck tN . AUS920030790US1

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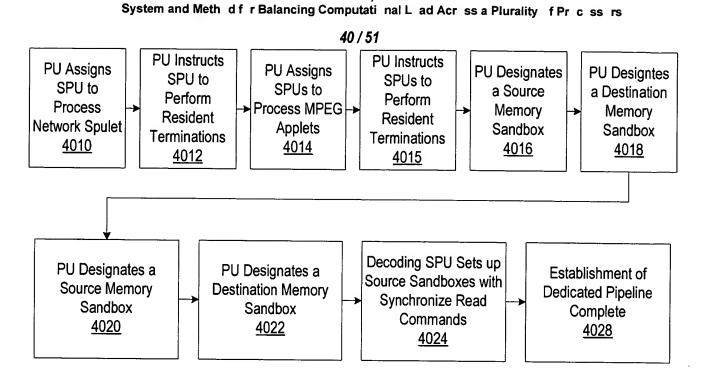


Figure 40A

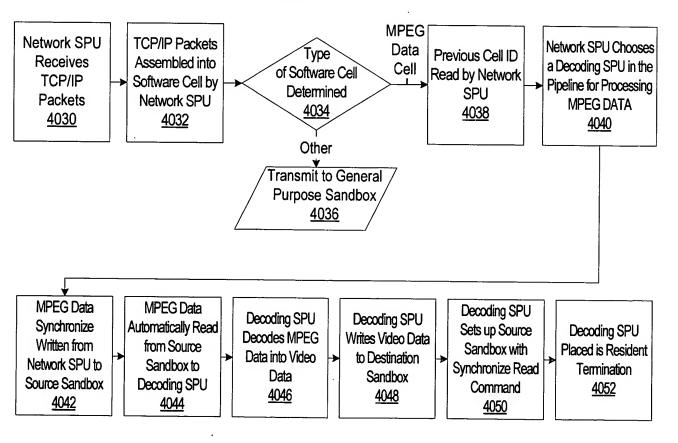


Figure 40B

Docket No. AUS920030790US1

Min r, tal.

Syst m and M th d f r Balancing Computational Load Acr ss a Plurality f Pr c ss rs

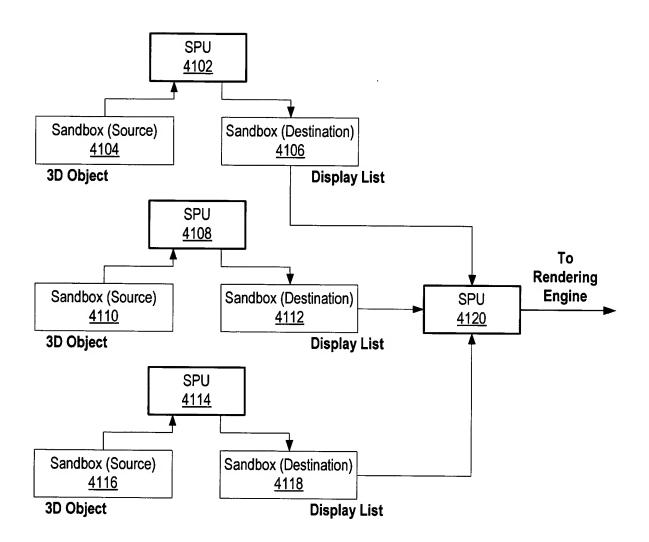


Figure 41

D ck t No. AUS920030790US1

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System and Meth d f r Balancing C mputati nal L ad Across a Plurality f Pr c ss rs

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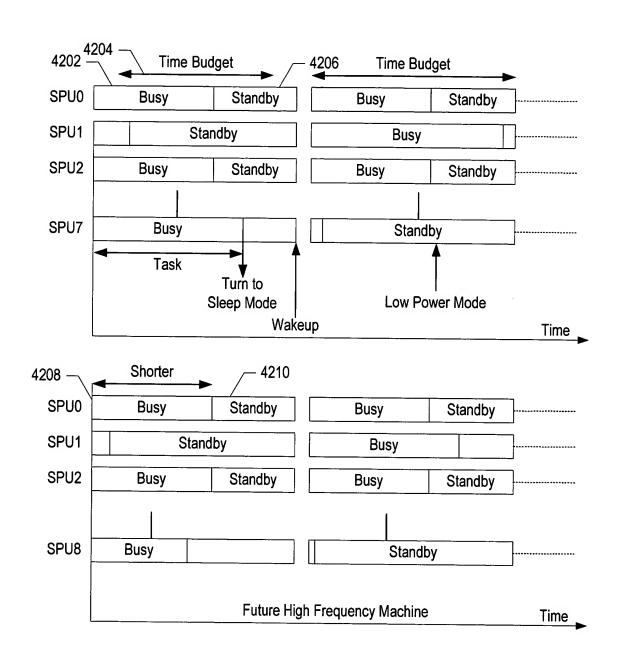
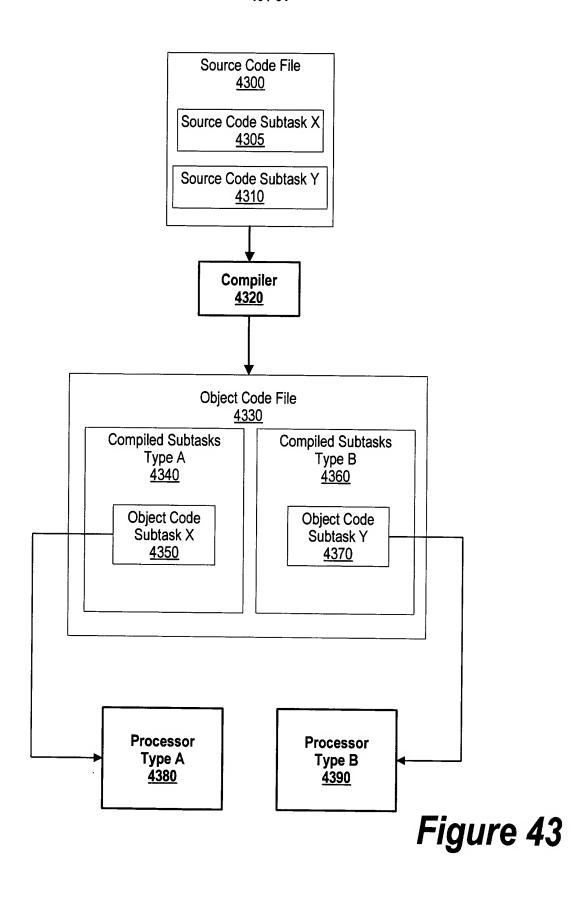


Figure 42

D cket No. AUS920030790US1

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System and Method for Balancing C mputati nal Load Acr ss a Plurality f Pr cessors



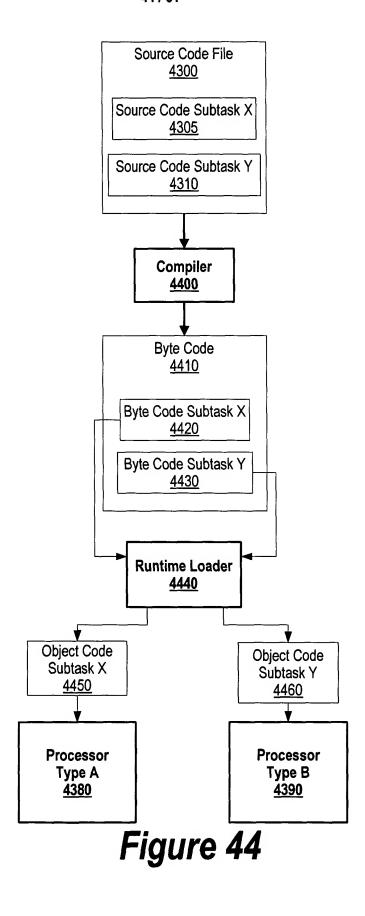
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Dock t No. AUS920030790US1

Min r, tal.

Syst m and Meth d for Balancing C mputati nal Load Acr ss a Plurality f Pr cess rs

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File: 3072.VSD

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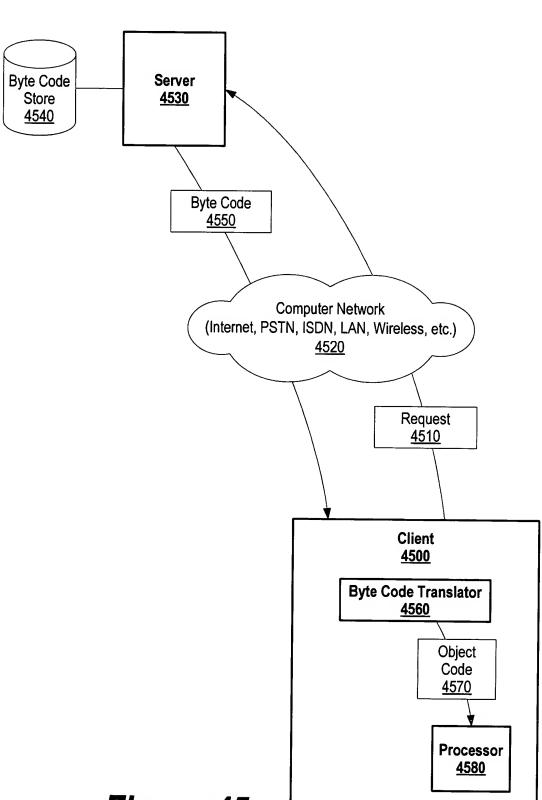


Figure 45

D cket No. AUS920030790US1

Min r, t al.

System and Method f r Balancing C mputati nal L ad Acr ss a Plurality f Pr cess rs

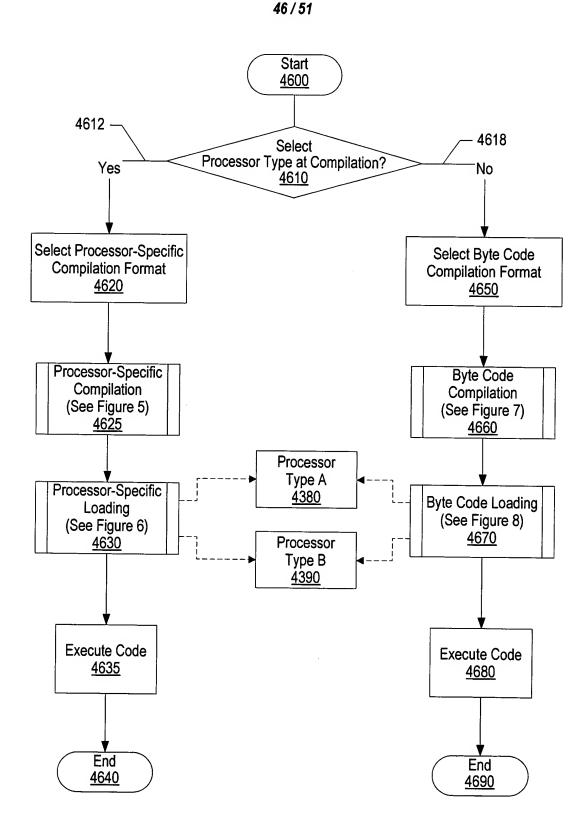
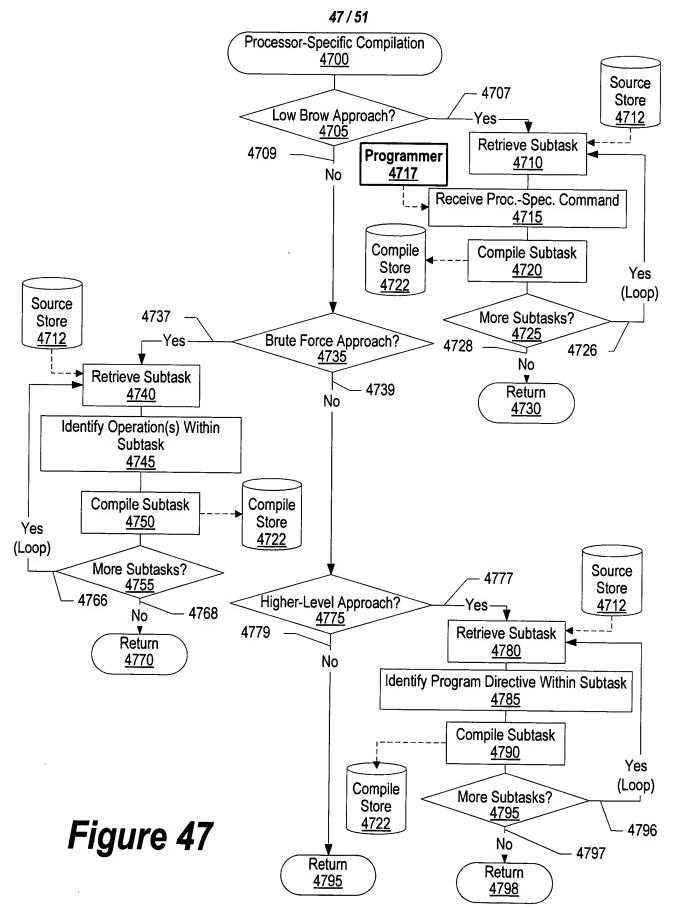


Figure 46

D cket No. AUS920030790US1

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Syst m and M th d f r Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs



D ck t N . AUS920030790US1

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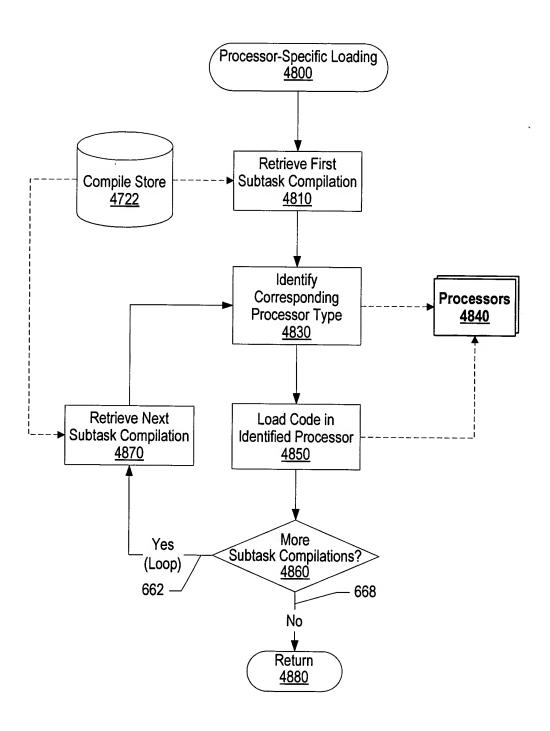
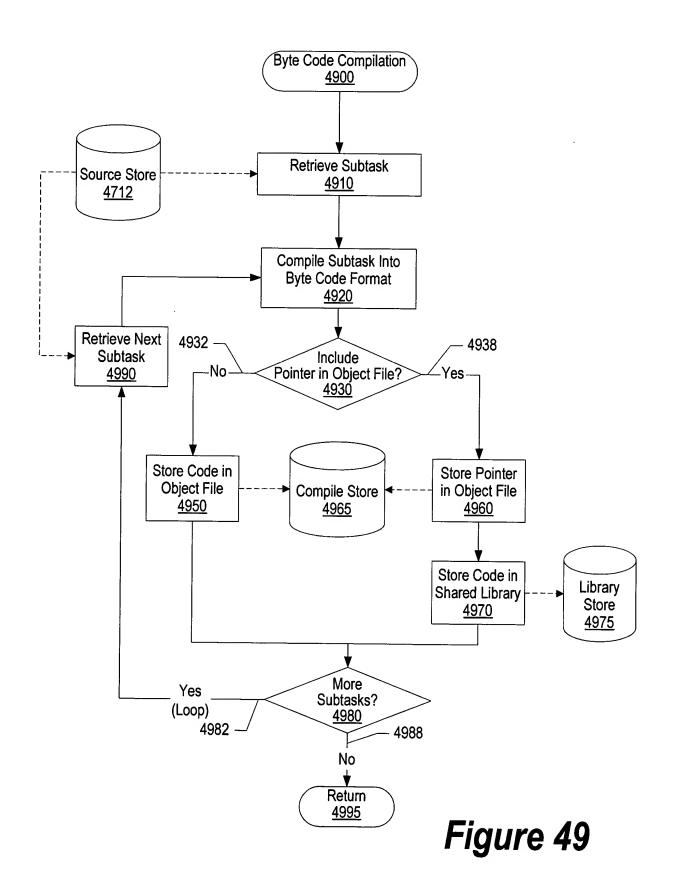


Figure 48

Docket No. AUS920030790US1

Min r, t al.

System and Meth d f r Balancing C mputati nal Load Acr ss a Plurality of Pr c ssors

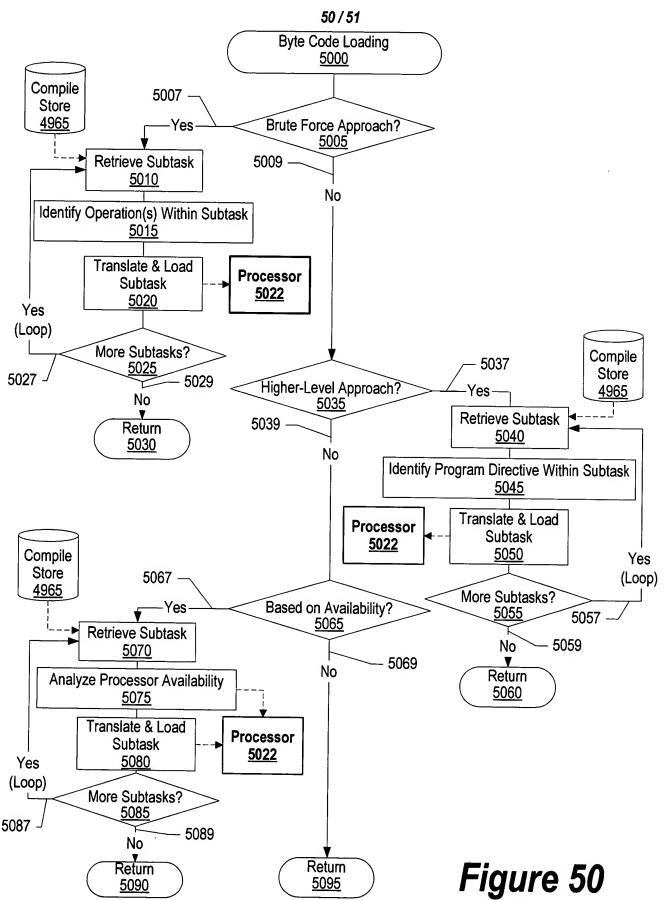


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Dock t N . AUS920030790US1

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Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs



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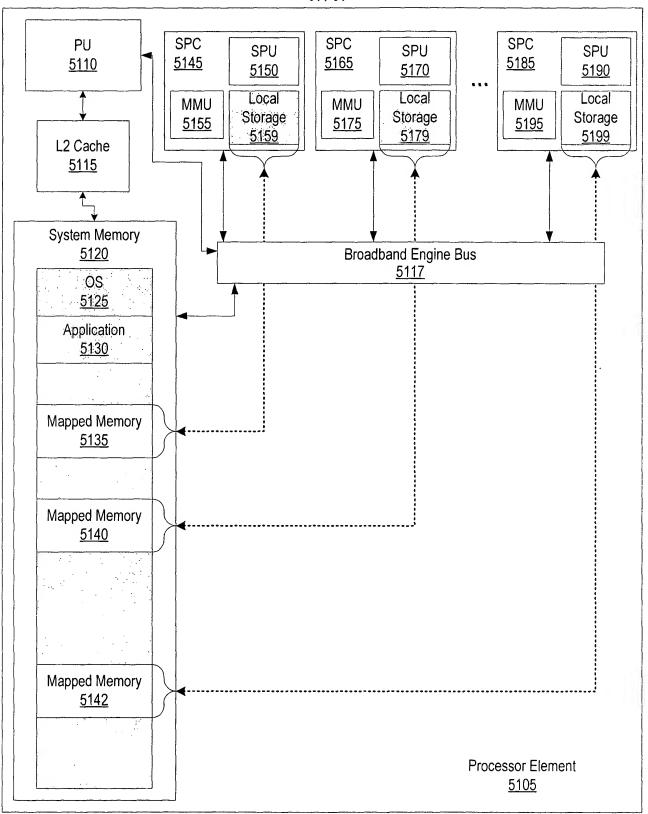


Figure 51